Electronic Acknowledgement Receipt				
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Application Number:	10734429			
International Application Number:				
Confirmation Number:	3875			
Title of Invention:	Thin-film transistor array with ring geometry			
First Named Inventor/Applicant Name:	William S. Wong			
Correspondence Address:	Patent Documentation Center Xerox Corporation Xerox Square 20th Floor 100 Clinton Ave. S. Rochester NY 14644 US -			
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1		A3602-US-NP_amd.pdf	109752	yes	3		
			4ffb1b776bf775fc37a9b6caa161e5879 7a4bbac				
	Multipa	rt Description/PDF files in	zip description				
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	Amendment - After Non-Final Rejection		1	1			
	Specification		2	2			
	Applicant Arguments/Remarks Made in an Amendment		3	3			
Warnings:							
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2 Information Disclosure Statement Letter	A3602-US-NP_Supp_IDS.pd f	155219	no	2			
		39lb27115l8c0s86792fce7e1fee3d3afe 49f365					
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3 Information Disclosure Statement (IDS) Filed	A3602-US-NP_PTO-1449.pd	109437	no	1			
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## National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/IDO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filling Receipt, in due course.

## New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.